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O- By Author	Neural Networks, 1993., IEEE International Conference on , 28 March-1 April 19
O- Basic O- Advanced	Page(s): 998 -1003 vol.2
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O Access the IEEE Member Digital Library	Gabrielli, A.; Gandolfi, E.; Masetti, M.; Maloberti, F.; Circuits and Systems, 1995., Proceedings., Proceedings of the 38th Midwest Symposium on , Volume: 1 , 13-16 Aug. 1995
Print Format	Page(s): 381 -384 vol.1

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3 Design of MIN/MAX cellular neural networks (MMCNNS) in CMOS tech Wen-Cheng Yen; Rong-Jian Chen; Jui-Lin Lai; Cellular Neural Networks and Their Applications, 2002. (CNNA 2002). Proceedin the 2002 7th IEEE International Workshop on , 22-24 July 2002 Page(s): 339 -346

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4 Fuzzy aggregating functions for multiobjective VLSI placement Khan, J.A.; Sait, S.M.;

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7 A design of current-mode analog circuits for fuzzy inference hardware systems

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8 Hardware fuzzy logic kit design

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Nojas, 1., Pelayo, P.J., Aliguita, M., Prieto, A., Microelectronics for Neural Networks and Fuzzy Sys

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21 A reconfigurable parallel inference processor for high speed fuzzy sy:

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Xi Chen; Dongming Jin; Zhijian Li;

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25 CCII-based fuzzy membership function and max/min circuits

Liu, S.I.; Hwang, Y.S.; Tsay, J.H.;

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Or Join IEEE Or Establish IEEE Web Account Or Access the IEEE Member Digital Library Experimental	[Abstract] [PDF Full-Text (464 KB)] IEEE JNL 27 Evaluation of min/max instructions for fuzzy information processing Watanabe, H.; Chen, D.; Konuri, S.; Fuzzy Systems, IEEE Transactions on , Volume: 4 Issue: 3 , Aug. 1996 Page(s): 369 -374 [Abstract] [PDF Full-Text (472 KB)] IEEE JNL 28 Pointer adaptation and pruning of min-max fuzzy inference and estin Arabshahi, P.; Marks, R.J., II; Seho Oh; Caudell, T.P.; Choi, J.J.; Bong-Gee Son Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transaction Volume: 44 Issue: 9 , Sept. 1997 Page(s): 696 -709
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The complexity of planar compliant motion planning under uncertainty

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B. R. Donald

Proceedings of the fourth annual symposium on Computational geometry January 1988 We consider the computational complexity of planning compliant motions in the plane, given geometric bounds on the uncertainty in sensing and control. We can give efficient algorithms for generating and verifying compliant motion strategies that are guaranteed to succeed as long as the sensing and control uncertainties lie within the specified bounds. We also consider the case where a compliant motion plan is required to succeed over some parametric family of geometries. While these problem ...

Proxies + path prediction: improving Web service provision in wireless-mobile communications 77% Stathes Hadjiefthymiades, Lazaros Merakos

Mobile Networks and Applications August 2003

Volume 8 Issue 4

Mobile computing is considered of major importance to the computing industry for the forthcoming years due to the progress in the wireless communications area. A proxy-based architecture for accelerating Web browsing in wireless customer premises networks is presented. Proxy caches, maintained in base stations, are constantly relocated to follow the roaming user. A cache management scheme is proposed, which involves the relocation of full caches to the most probable cells but also percentages of ...

Fuzzy maps and their application in the simplification of fuzzy switching functions Abraham Kandel

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Proceedings of the sixth international symposium on Multiple-valued logic May 1976 In Boolean logic, a Karnaugh map may be regarded either as a pictorial form of a trugh table, or as an extension of the Venn diagram. However, when fuzzy logic is concerned another minimization method is required, and therefore an extension of a Karnaugh map is investigated.

In this paper a new minimization algorithm is developed in order to remove the existing disadvantage of simplifying fuzzy forms. The algorithm is based on a new representation of fuzzy forms that a ...

Abstracts: East Coast Computer Algebra day: ECCAD 2002 poster and demonstration

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William Y. Sit

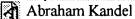
ACM SIGSAM Bulletin September 2002

Volume 36 Issue 3

East Coast Computer Algebra Day 2002 (ECCAD 02) was held on May 18, 2002 at LaGuardia Community College of The City University of New York, New York. The abstracts of the invited speakers were published in the March 2002 issue of this Bulletin. Below are the abstracts of posters and demonstrations that were accepted and presented at the conference (some posters were presented in absentia).

Imprecise models in combinational systems

77%



Proceedings of the 17th annual Southeast regional conference April 1979

The theory of fuzzy switching functions described in this paper is related to the theory of fuzzy sets and to the treatment of switching circuits in the binary world. In this paper we are concerned with the study of such imprecise mechanisms, their properties, and possible applications. The enumeration of the number of distinct fuzzy switching functions will be addressed as well as minimization and simplification procedures.

HTTP Cookies: Standards, privacy, and politics

77%



David M. Kristol

ACM Transactions on Internet Technology (TOIT) November 2001

Volume 1 Issue 2

How did we get from a world where cookies were something you ate and where "nontechies" were unaware of "Netscape cookies" to a world where cookies are a hot-button privacy issue for many computer users? This article describes how HTTP "cookies" work and how Netscape's original specification evolved into an IETF Proposed Standard. I also offer a personal perspective on how what began as a straightforward technical specification turned into a political flashpoint when it tried to address nontechn...

Computing curricula 2001

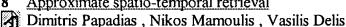
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Journal on Educational Resources in Computing (JERIC) September 2001

Approximate spatio-temporal retrieval

77%



ACM Transactions on Information Systems (TOIS) January 2001

Volume 19 Issue 1

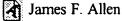
This paper proposes a framework for the handling of spatio-temporal queries with inexact matches, using the concept of relation similarity. We initially describe a binary string encoding for 1D relations that permits the automatic derivation of similarity measures. We then extend this model to various granularity levels and many dimensions, and show that reasoning on spatio-temporal structure is significantly facilitated in the new framework. Finally, we provide



algorithms and optimization ...

9 Maintaining knowledge about temporal intervals

77%



Communications of the ACM November 1983

Volume 26 Issue 11

10 On becoming virtual: the driving forces and arrangements

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Magid Igbaria, Conrad Shayo, Lorne Olfman

Proceedings of the 1999 ACM SIGCPR conference on Computer personnel research April 1999

11 Nested maps—a formal, provably correct object model for spatial aggregates

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Lutz Plümer, Gerhard Gröger

Proceedings of the fourth ACM workshop on Advances in geographic information systems November 1996

12 From trees into boxes

77%

David Steinbrook, Eugene McDonnell

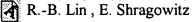
ACM SIGAPL APL Quote Quad, Proceedings of the international conference on APL September 1993

Volume 24 Issue 1

This paper is a progress report on work undertaken to include tree data structures by means of the boxed data type available in J. Methods for displaying these boxed arrays as trees are shown. This work is part of a larger effort to provide a comprehensive set of facilities in J for working with tree structures. The facilities described were at first modelled in J and subsequently translated into C, in order to provide a J interpreter which has trees as native facilities. Thus this work also exe ...

13 Fuzzy logic approach to placement problem

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Dana Angluin

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15 Expert system on a chip: an engine for real-time approximate reasoning

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M Togai, H Watanabe

Proceedings of the ACM SIGART international symposium on Methodologies for intelligent systems December 1986

The role of inferencing with uncertainty is becoming more important in rule-based expert systems (ES), since knowledge given by a human expert is often uncertain or imprecise. We have succeeded in designing a VLSI chip which can perform an entire inference process based



on fuzzy logic. The design of the VLSI fuzzy inference engine emphasizes simplicity, extensibility, and efficiency (operational speed and layout area). It is fabricated in 2.5 &mgr;m CMOS technology. The inference engine con ...

16 The network architecture of the Connection Machine CM-5 (extended abstract)

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Charles E. Leiserson, Zahi S. Abuhamdeh, David C. Douglas, Carl R. Feynman, Mahesh N. Ganmukhi, Jeffrey V. Hill, Daniel Hillis, Bradley C. Kuszmaul, Margaret A. St. Pierre, David S. Wells, Monica C. Wong, Shaw-Wen Yang, Robert Zak

Proceedings of the fourth annual ACM symposium on Parallel algorithms and architectures June 1992

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3 Technology mapping using fuzzy logic

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83%

Sasan Iman, Massoud Pedram, Kamal Chaudhary

Proceedings of the 31st annual conference on Design automation conference June 1994

4 On the hardware-software partitioning problem: System modeling and partitioning techniques

Marisa López-Vallejo, Juan Carlos López

ACM Transactions on Design Automation of Electronic Systems (TODAES) July 2003 Volume 8 Issue 3

1 of 5

This paper presents an in-depth study of several system partitioning procedures. It is based on the appropriate formulation of a general system model, being therefore independent of either the particular co-design problem or the specific partitioning procedure. The techniques under study are a knowledge-based system and three classical circuit partitioning algorithms (Simulated Annealing, Kernighan&Lin and Hierarchical Clustering). The former has been entirely proposed by the authors in previous ...

Expert system on a chip: an engine for real-time approximate reasoning

83%



M Togai, H Watanabe

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The role of inferencing with uncertainty is becoming more important in rule-based expert systems (ES), since knowledge given by a human expert is often uncertain or imprecise. We have succeeded in designing a VLSI chip which can perform an entire inference process based on fuzzy logic. The design of the VLSI fuzzy inference engine emphasizes simplicity, extensibility, and efficiency (operational speed and layout area). It is fabricated in 2.5 &mgr,m CMOS technology. The inference engine con ...

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R.-B. Lin, E. Shragowitz

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The theory of fuzzy switching functions described in this paper is related to the theory of fuzzy sets and to the treatment of switching circuits in the binary world. In this paper we are concerned with the study of such imprecise mechanisms, their properties, and possible applications. The enumeration of the number of distinct fuzzy switching functions will be addressed as well as minimization and simplification procedures.

Design of an adaptive motors controller based on fuzzy logic using behavioral synthesis

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A. Changuel, A. Jerraya, R. Rolland

Proceedings of the conference with EURO-VHDL'96 and exhibition on European Design Automation September 1996

Impulse response fault model and fault extraction for functional level analog circuit diagnosis

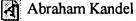
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Chauchin Su, Shenshung Chiang, Shyh-Jye Jou

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Proceedings of the sixth international symposium on Multiple-valued logic May 1976 In Boolean logic, a Karnaugh map may be regarded either as a pictorial form of a trugh table, or as an extension of the Venn diagram. However, when fuzzy logic is concerned another minimization method is required, and therefore an extension of a Karnaugh map is investigated. In this paper a new minimization algorithm is developed in order to remove the existing disadvantage of simplifying fuzzy forms. The algorithm is based on a new representation of

11 Optimal precision in the presence of uncertainty

80%



J Y Halpern, N Megiddo, A A Munshi

fuzzy forms that a ...

Proceedings of the seventeenth annual ACM symposium on Theory of computing December 1985

We consider the problem of achieving coordinated actions in a real-time distributed system. In particular, we consider how tightly processors can be guaranteed to perform a particular action, in a system where message transmission is guaranteed, but there is some uncertainty in message transmission time. We present an algorithm to achieve optimal precision in arbitrary networks.

12 Procedure cloning: a transformation for improved system-level functional partitioning Frank Vahid

80%



ACM Transactions on Design Automation of Electronic Systems (TODAES) January 1999

Volume 4 Issue 1

Functional partitioning assigns the functions of a system's program-like specification among system components, such as standard-software and custom-hardware processors. We introduce a new transformation, called procedure cloning, that significantly improves functional partitioning results. The transformation creates a clone of a procedure for sole use by a particular procedure caller, so the clone can be assigned to the caller's processor, which in turn improves performance through reduced ...

13 Supervised adaptive resonance networks

80%



R. S. Baxter

Proceedings of the conference on Analysis of neural network applications May 1991

14 The B-ternary logic and its applications to the detection of hazards in combinational switching

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Masao Mukaidono

Proceedings of the eighth international symposium on Multiple-valued logic January 1978 Both the steady states and some transient states of switching circuits can be described by B-ternary logic in which the truth values 0, 1 and 1/2 are used respectively to represent false, true and uncertainty. This paper showed the methods of detecting and identifying various kinds of static hazards contained in combinational switching circuits by means of the canonical forms of the B-ternary logic functions realized by the circuits. Particularly, a method was derived which could algebraica ...

15 N-variable fuzzy maps with application to disjunctive decomposition of fuzzy switching functions

77%



Gary W. Schwede

Proceedings of the sixth international symposium on Multiple-valued logic May 1976

3 of 5

A graphical scheme (map) for representation and manipulation of fuzzy switching functions of N-variables is described. Properties of the map and relations between represented implicants are discussed. Emphasis is placed on illustrations of the use of the map for graphical minimization and decomposition of fuzzy switching functions.

16 Some algebraic and combinatorial aspects of Multiple-valued circuits

77%

I. G. Rosenberg

Proceedings of the sixth international symposium on Multiple-valued logic May 1976 The purpose of this expository paper is to review some algebraic and combinatorial results arising in the theory of multiple-level switching circuits. Due to space limitations a selection from the surprisingly rich literature had to be made: the trends and topics presented at the past five International Symposia on Multiple-valued logic. The discussion centers on the formulation of basic problems rather than on the presentation of particular results which may be found in a detailed bibliogr ...

17 Optimizing exact genetic linkage computations

77%

Maáyan Fishelson, Dan Geiger

Proceedings of the seventh annual international conference on Computational molecular biology April 2003

Genetic linkage analysis is a challenging application which requires Bayesian networks consisting of thousands of vertices. Consequently, computing the likelihood of data, which is needed for learning linkage parameters, using exact inference procedures calls for an extremely efficient implementation that carefully optimizes the order of conditioning and summation operations. In this paper we present the use of stochastic greedy algorithms for optimizing this order. Our algorithm has been incorp ...

18 CANDIDE: a learning system for process control

77%



B. Burg, D. Luzeaux, B. Zavidovique

Proceedings of the second international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 June 1989 The aim of this paper is to present an application of artificial intelligence techniques to control. Their use at a high level, as supervisor tools is shortly described and we focuse the attention onto their use at low level, inside the control loops. We describe our approach using artificial

intelligence machine learning to acquire knowledge concerning the controlled system, to modelise it and finally to control it. As an example, CANDIDE learns to drive a car. We explain all the learning ...

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19 A backend machine architecture for information retrieval

Amar Mukhopadhyay

Proceedings of the 3rd annual ACM conference on Research and development in information retrieval June 1980

20 Process variation: Explicit computation of performance as a function of process variation

77%

Lou Scheffer

Proceedings of the 8th ACM/IEEE international workshop on Timing issues in the specification and synthesis of digital systems December 2002

Each manufactured chip is a little bit different, and designers want as many as possible of these

chips to work. Process variation is a function of many variables, as the width, thickness, and inter-layer thickness can vary independently for each layer on a chip, as can temperature and voltage. Currently designers cope with this by picking a few subsets of these conditions, called process corners, and analyzing at these conditions. However, it's easy to show this approach is both too conservativ ...

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21 Innovative Applications: A dynamically reconfigurable adaptive viterbi decoder

77%

Sriram Swaminathan, Russell Tessier, Dennis Goeckel, Wayne Burleson Proceedings of the 2002 ACM/SIGDA tenth international symposium on Field-programmable gate arrays February 2002

The use of error-correcting codes has proven to be an effective way to overcome data corruption in digital communication channels. Although widely-used, the most popular communications decoding algorithm, the Viterbi algorithm, requires an exponential increase in hardware complexity to achieve greater decode accuracy. In this paper, we describe the analysis and implementation of a reduced-complexity decode approach, the adaptive Viterbi algorithm (AVA). Our AVA design is implemented in reconfigu ...

22 A hardware/software co-design flow and IP library based on simulink

77%

L. M. Reyneri, F. Cucinotta, A. Serra, L. Lavagno

Proceedings of the 38th conference on Design automation June 2001

This paper describes a design flow for data-dominated embedded systems. We use The Mathworks' Simulink\trademark environment for functional specification and algorithmic analysis. We developed a library of Simulink blocks, each parameterized by design choices such as implementation (software, analog or digital hardware, \ldots) and numerical accuracy (resolution, S/N ratio). Each block is equipped with empirical models for cost (code size, chip area) and performance (timing, energy), based ...

23 Illustrative risks to the public in the use of computer systems and related technology

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Peter G. Neumann

ACM SIGSOFT Software Engineering Notes January 1996

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Volume 21 Issue 1

24 Fuzzy-logic digital-analogue interfaces for accurate mixed-signal simulation

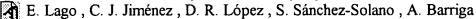
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Proceedings of the conference on Design, automation and test in Europe February 1998 A new approach to mixed-signal circuit interfacing based on fuzzy logic models is presented. Due to their continuous rather than discrete character, fuzzy logic models offer a significant improvement compared with the classical D-A interface models. Fuzzy logic D-A interfaces can represent the boundary between the digital and analogue worlds accurately without a significant loss of computational efficiency. The potential of mixed-signal interfacing based on fuzzy logic is demonstrated by an exam ...

25 XFVHDL: a tool for the synthesis of fuzzy logic controllers

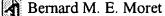
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Proceedings of the conference on Design, automation and test in Europe February 1998 A tool for the synthesis of fuzzy controllers is presented in this paper. This tool takes as input the behavioral specification of a controller and generates its VHDL description according to a target architecture. The VHDL code can be synthesized by means of two implementation methodologies, ASIC and FPGA. The main advantages of using this approach are rapid prototyping, and the use of well-known commercial design environments like Synopsys, Mentor Graphics, or Cadence.

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Dimitris Papadias

Proceedings of the 23rd annual international ACM SIGIR conference on Research and development in information retrieval July 2000

The retrieval of stored images matching an input configuration is an important form of content-based retrieval. Exhaustive processing (i.e., retrieval of the best solutions) of configuration similarity queries is, in general, exponential and fast search for sub-optimal solutions is the only way to deal with the vast (and ever increasing) amounts of multimedia information in several real-time applications. In this paper we discuss the utilization of hill climbing heuristics that can provide ve ...

29 Hardware/software synthesis of formal specifications in codesign of embedded systems

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Vincenza Carchiolo , Michele Malgeri , Guiseppe Mangioni

ACM Transactions on Design Automation of Electronic Systems (TODAES) July 2000 Volume 5 Issue 3

CoDesign aims to integrate the design techniques of hardware and software. In this work, we present a CoDesign methodology based on a formal approach to embedded system specification. This methodology uses the Templated T-LOTOS language to specify the system during all design phases. Templated T-LOTOS is a formal language based on CCS and CSP models. Using Templated T-LOTOS, a system can be specified by observing the temporal ordering in which the events occur from the outside. In this pape ...

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Steven L. Horowitz, Theodosios Pavlidis

Journal of the ACM (JACM) April 1976

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In the past, picture segmentation has been performed by merging small primitive regions or by recursively splitting the whole picture. This paper combines the two approaches with significant increase in processing speed while maintaining small memory requirements. The data structure is described in detail and examples of implementations are given.

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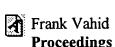
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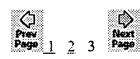
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S. Keshav

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Reimund Wittmann, Bedrich Hosticka, Michael Schanz, Werner Schardein, Stefan Kern, Reinhold Vahrmann

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Simon S. Lam, Simon Chow, David K. Y. Yau

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Interframe compression techniques, such as those used in MPEG video, give rise to a coded bit stream where picture sizes differ by a factor of 10 or more. As a result, buffering is needed to reduce (smooth) rate fluctuations of encoder output from one picture to the next; without

smoothing, the performance of networks that carry such video traffic would be adversely affected. Various techniques have been suggested for controlling the output rate of a VBR encoder to alleviate network congest ...

44 A new optimizer for performance optimization of analog integrated circuits N. S. Nagaraj Proceedings of the 30th international on Design automation conference July 1993 77% 45 VHDL and fuzzy logic if-then rules Alex Zamfirescu, Cary Ussery Proceedings of the conference on European Design Automation November 1992 77% 46 GOSSIP: a generic system for statistical circuit design L. J. Opalski, M. A. Styblinski Proceedings of the conference on European Design Automation November 1992 77% 47 A control-theoretic approach to flow control A Srinivasan Keshav ACM SIGCOMM Computer Communication Review, Proceedings of the conference on Communications architecture & protocols August 1991 Volume 21 Issue 4 77% 48 Automated selection of mathematical software Michael Lucks, Ian Gladwell ACM Transactions on Mathematical Software (TOMS) March 1992 Volume 18 Issue 1 Current approaches to recommending mathematical software are qualitative and categorical. These approaches are unsatisfactory when the problem to be solved has features that can "trade-off" in the recommendation process. A quantitative system is proposed that permits tradeoffs and can be built and modified incrementally. This quantitative approach extends other knowledge-engineering techniques in its knowledge representation and aggregation facilities. The system is demonstrated ...

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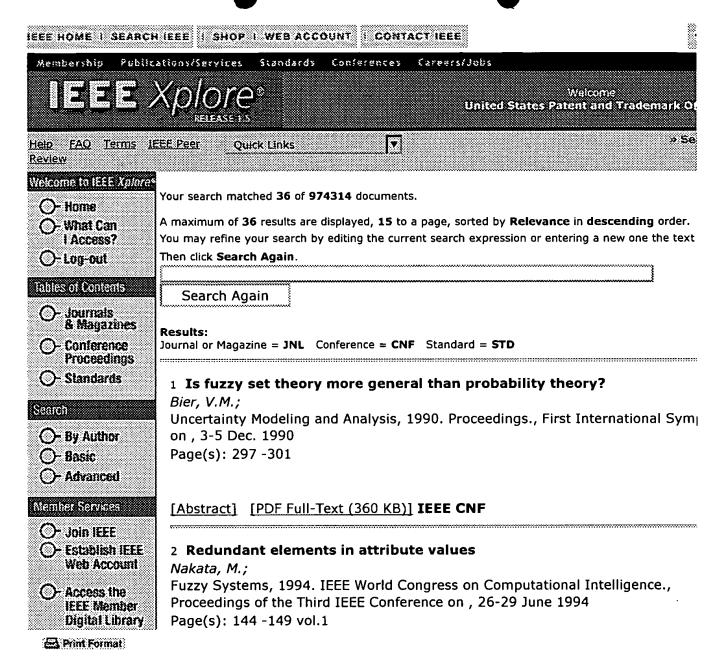


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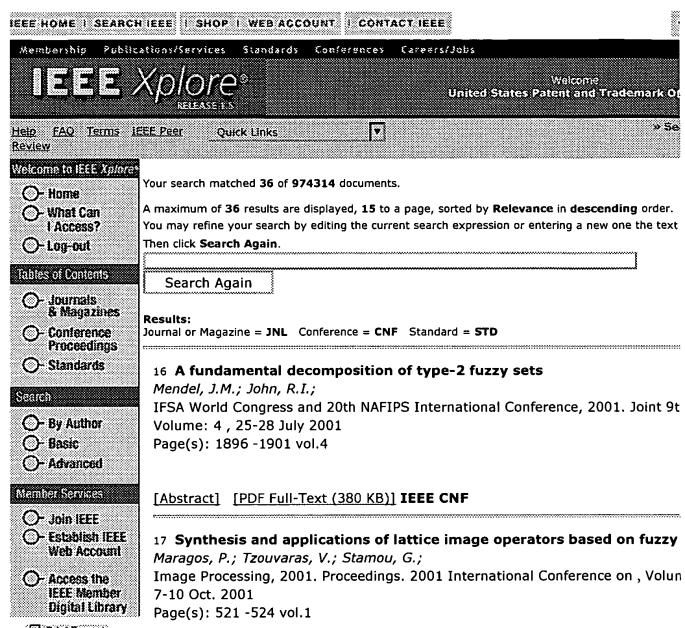
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PCT #: NONE Publication #: NONE Pub Dt:

Inventors: Francesco Pappalardo, Biagio Giacalone, Francesco Mammoliti, Edmondo Gangi

Title: Logical fuzzy union and intersection operation calculation circuit

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